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Class	Subclass	ISSUE CLASSIFICATION

PATENT NUMBER

## U.S. UTILITY Patent Application

<p>② <i>gk</i></p> <p>O.I.P.E.</p> <p>SCANNED <i>AK</i> Q.A. <i>21</i></p>	PATENT DATE
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CI	APPLICATION NO. 09/604519	CONT/PRIOR	CLASS 324 257	SUBCLASS 779	ART UNIT 2838 2814	EXAMINER G. KAYBUL
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## APPLICANTS

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**TITLE**

integrated circuit and a method of manufacturing an integrated circuit

PTO-2040  
12/99[illegible]

<input type="checkbox"/> <b>TERMINAL DISCLAIMER</b>	<b>DRAWINGS</b>		<b>CLAIMS ALLOWED</b>	
	<b>Sheets Drwg.</b>	<b>Figs. Drwg.</b>	<b>Print Fig.</b>	<b>Total Claims</b>
<input type="checkbox"/> The term of this patent subsequent to _____ (date) has been disclaimed.  <input type="checkbox"/> The term of this patent shall not extend beyond the expiration date of U.S Patent. No. _____ _____ _____	_____ (Assistant Examiner)		<b>NOTICE OF ALLOWANCE MAILED</b>	
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<input type="checkbox"/> The terminal ____ months of this patent have been disclaimed.	_____ (Legal Instruments Examiner)		<b>ISSUE BATCH NUMBER</b>	
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